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December 9, 2004

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MESSAGE:

Serial No.: 10/739,229
Confirmation: 1123
Applicant: Byoung Hee Cho
Filed: December 18, 2003
Title: METHOD FOR FORMING GATE OXIDE IN SEMICONDUCTOR DEVICE
Art Unit: 2812
Examiner: Not yet assigned
Atty Docket: 29936/39870
Customer No.: 04743

Enclosed is a copy of a Status Inquiry (1 page).

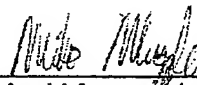
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Serial No.	: 10/739,229)	I hereby certify that this paper and all
Confirmation	: 1123)	documents referred to herein as being
)	enclosed are being transmitted to the
Applicants	: Byoung Hee Cho)	United States Patent and Trademark Office
Filed	: December 18, 2003)	via facsimile on December 9, 2004 at
)	facsimile number (703) 872-9306.
Title:	METHOD FOR FORMING GATE)	
	OXIDE IN SEMICONDUCTOR)	
	DEVICE)	
)	
Art Unit	: 2812)	
Examiner	: Not yet assigned)	
)	Michael Muczyński
Atty Docket	: 29936/39870)	
Customer No.	: 04743)	

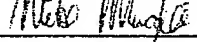
STATUS INQUIRY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Please advise when an official action can be expected in the above-identified application.

Respectfully submitted,
MARSHALL, GERSTEIN & BORUN LLP

By: 
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Reg. No. 48,642
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December 9, 2004

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